

FIG. 1
(PRIOR ART)

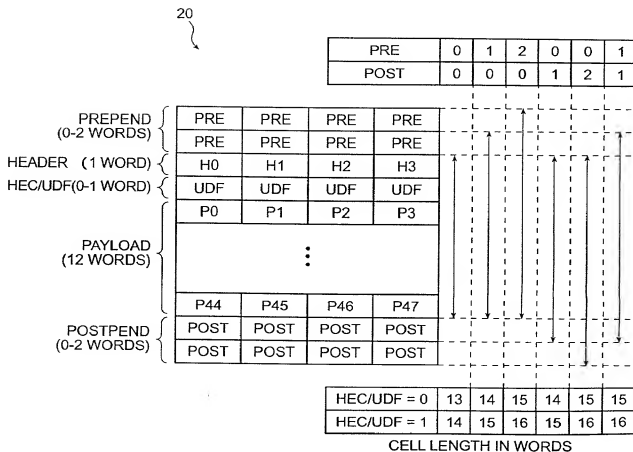


FIG. 2
(PRIOR ART)

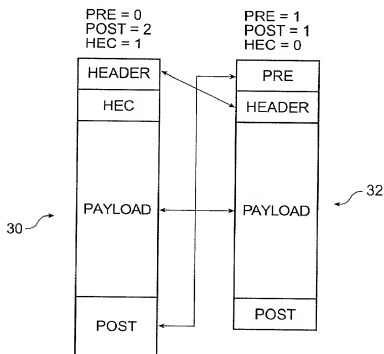


FIG. 3
(PRIOR ART)

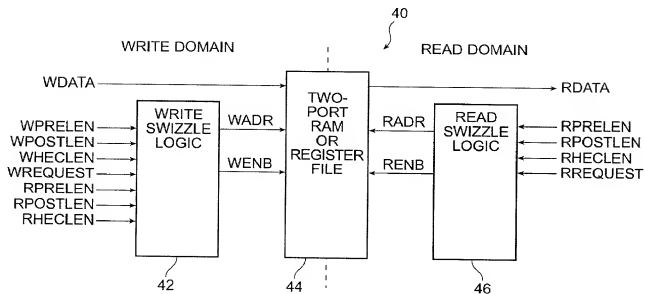


FIG. 4
(PRIOR ART)

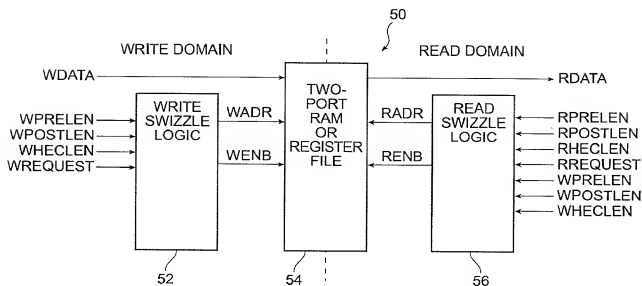


FIG. 5
(PRIOR ART)

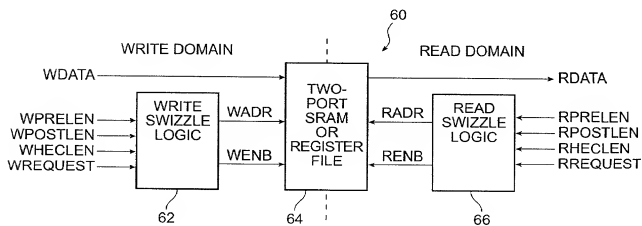


FIG. 6

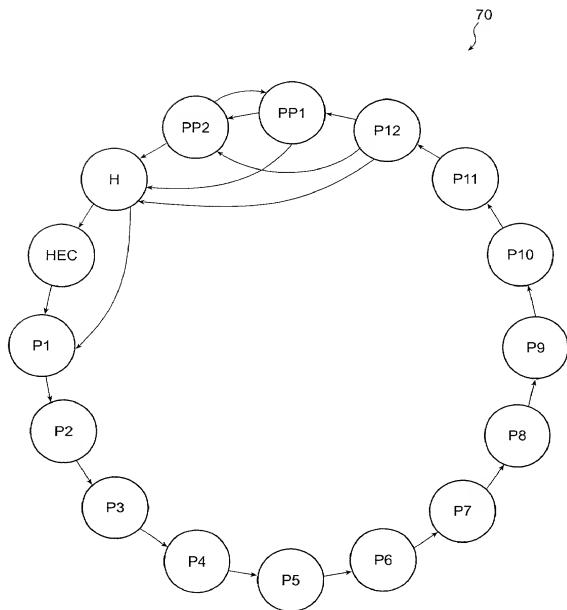


FIG. 7

100250 92655360

WRITE STATE TRANSITION TABLE
 WPRELEN = 0
 WPOSTLEN = 2
 WHEELLEN = 1

MEMORY

READ STATE TRANSITION TABLE
 RPRELEN = 1
 RPOSTLEN = 1
 RHEELLEN = 0

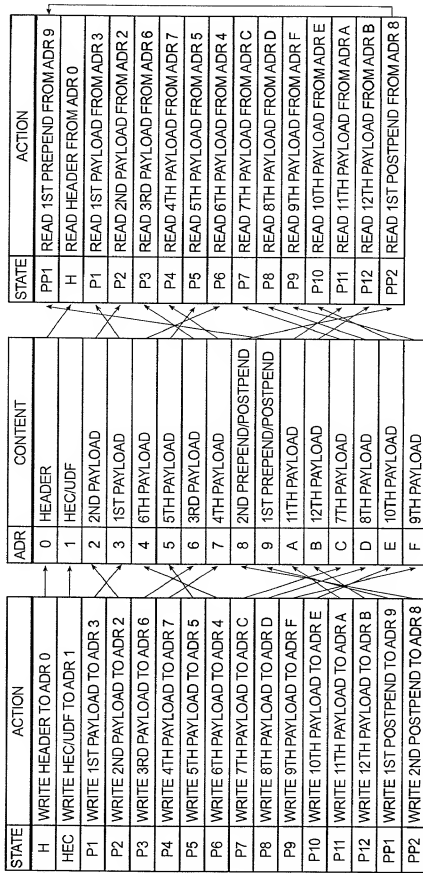


FIG. 8